

# **SWAMI VIVEKANAND UNIVERSITY, SIRONJA, SAGAR (M.P.)**



## **SCHEME**

**For**

**MASTER OF TECHNOLOGY (M.Tech.)  
DIGITAL COMMUNICATION**

**Course Code : MTDC**

Department of Electronics & Communication  
Engineering  
Faculty of Engineering

Duration of Course : 2 Year
Examination Mode : Semester
Examination System : Grading

Swami Vivekanand University, Sironja Sagar (M.P.)

2016-2017



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering  
Scheme of Course : M.Tech. – Digital Communication. Course Code : MTDC

Department of Electronics & Communication Engineering  
Semester :- 1<sup>st</sup> Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks											Duration of Theory Exam
		L	T	P		Theory					Practical					Grand Total (I= D+H)	
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (H= E+F+ G)		
						Max (A)	Min	MST (B)	TW (C)		Max (E)	Min	LW (F)	PQ (G)			
MTDC-0101	Advanced Mathematics	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0102	Micro controller system Design	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0103	DSP Application	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0104	VLSI Design	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0105	Data Communication and Computer Networks	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0106	Lab-I (102,103)	-	-	6	6	-	-	-	-	-	90	36	-	60	150	150	-
MTDC-0107	Lab-II (104,105)	-	-	6	6	-	-	-	-	-	90	36	-	60	150	150	-
	Total	15	05	12	32	350	-	100	50	500	180	-	-	120	300	800	



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering  
Scheme of Course : M.Tech. - Digital Communication.

Department of Electronics & Communication Engineering  
Course Code : MTDC  
Semester :- 2<sup>nd</sup> Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks											Duration of Theory Exam
		L	T	P		Theory					Practical					Grand Total (I= D+H)	
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (H= E+F+G)		
						Max (A)	Min	MST (B)	TW (C)		Max (E)	Min	LW (F)	PQ (G)			
MTDC-0201	Nano Technology	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0202	Modelling & Simulation of Computer	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0203	Network Design Technology	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0204	Optical Network	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0205	Mobile & Satellite Communication	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0206	Lab-III (202)	-	-	6	6	-	-	-	-	-	90	36	-	60	150	150	-
MTDC-0207	Lab-IV (203)	-	-	6	6	-	-	-	-	-	90	36	-	60	150	150	-
	<b>TOTAL</b>	<b>15</b>	<b>5</b>	<b>12</b>	<b>32</b>	<b>350</b>		<b>100</b>	<b>50</b>	<b>500</b>	<b>180</b>	<b>-</b>	<b>-</b>	<b>120</b>	<b>300</b>	<b>800</b>	



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering

Department of Electronics & Communication Engineering

Scheme of Course : M.Tech. - Digital Communication. Course Code : MTDC

Semester :- 3<sup>rd</sup> Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks											Duration of Theory Exam
		L	T	P		Theory					Practical					Grand Total (I= D+H)	
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (H= E+F+G)		
						Max (A)	Min	MST (B)	TW (C)		Max (E)	Min	LW (F)	PQ (G)			
MTDC-301	Information Theory & Coding	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0302	Elective I	3	1	-	4	70	28	20	10	100	-	-	-	-	-	100	3 Hrs
MTDC-0303	Seminar	-	-	4	4	-	-	-	-	-	-	-	-	100	100	100	-
MTDC-0304	Dissertation Part I	-	-	8	8	-	-	-	-	-	120	48	-	80	200	200	-
	<b>Total</b>	<b>06</b>	<b>02</b>	<b>12</b>	<b>20</b>	<b>140</b>	<b>-</b>	<b>40</b>	<b>20</b>	<b>200</b>	<b>120</b>	<b>-</b>	<b>-</b>	<b>180</b>	<b>300</b>	<b>500</b>	

### Elective-I

MTDC-0302(A) Advanced Digital Communication

MTDC-0302(B) Optical Instrumentation & Measurement



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering  
Scheme of Course : M.Tech. - Digital Communication.

Department of Electronics & Communication Engineering  
Course Code : MTDC  
Semester :- 4<sup>th</sup> Sem

S.No.	Subject Code	Subject Name	Periods per week			Credits	Maximum Marks (Theory Slot)			Maximum Marks (Practical Slot)		Total Marks
			L	T	P		End Sem. Exam.	Tests (Two)	Assignments /Quiz	End Sem. Practical/Viva	Practical Record/Assignment/Quiz/Presentation	
1.	MTDC401	Dissertation Part- II	-	-	20	20	-	-	-	300	200	500
		Total	-	-	20	20	-	-	-	300	200	500

**L: Lecture - T: Tutorial - P: Practical**